

Edgecard pin #	Side B Description	Name	CEM pin #
B01	+12 volt power	+12v	B01
B02	+12 volt power	+12v	B02
B03	+12 volt power	+12v	B03
B04	Ground	GND	B04
B05	SMBus clock	SMCLK	B05
B06	SMBus data	SMDAT	B06
B07	Ground	GND	B07
B08	+3.3 volt power	+3.3v	B08
B09	+TRST#	JTAG1	B09
B10	3.3v auxiliary power	3.3Vaux	B10
B11	Link Reactivation	WAKE#	B11
	Mechanical Key		
B12	Clock Request Signal	CLKREQ#	B12
B13	Ground	GND	B13
B14	Transmitter Lane 0, Differential	PETp(0)	B14
B15	pair	PETn(0)	B15
B16	Ground	GND	B16
B17	Presence detect	PRSNT2#	B17
B18	Ground	GND	B18
B19	Transmitter Lane 1, Differential	PETp(1)	B19
B20	pair	PETn(1)	B20
B21	Ground	GND	B21
B22	Ground	GND	B22
B23	Transmitter Lane 2, Differential	PETp(2)	B23
B24	pair	PETn(2)	B24
B25	Ground	GND	B25
B26	Ground	GND	B26
B27	Transmitter Lane 3, Differential	PETp(3)	B27
B28	pair	PETn(3)	B28
B29	Ground	GND	B29
B30	Emergency Pwr Reduct	PWRBRK#	B30
B31	Presence detect	PRSNT2#	B31
B32	Ground	GND	B32
B33	Transmitter Lane 4, Differential	PETp(4)	B33
B34	pair	PETn(4)	B34
B35	Ground	GND	B35
B36	Ground	GND	B36
B37	Transmitter Lane 5, Differential	PETp(5)	B37
B38	pair	PETn(5)	B38
B39	Ground	GND	B39
B40	Ground	GND	B40
B41	Transmitter Lane 6, Differential	PETp(6)	B41
B42	pair	PETn(6)	B42
B43	Ground	GND	B43
B44	Ground	GND	B44
B45	Transmitter Lane 7, Differential	PETp(7)	B45
B46	pair	PETn(7)	B46
B47	Ground	GND	B47
B48	Presence detect	PRSNT2#	B48
B49	Ground	GND	B49
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Edgecard pin #	Side A Description	Name	CEM pin #
A01	Presence detect	PRSNT#1	A01
A02	+12 volt power	+12v	A02
A03	+12 volt power	+12v	A03
A04	Ground	GND	A04
A05	TCK	JTAG2	A05
A06	TDI	JTAG2	A05
A07	TDO	JTAG3	A00
A07 A08	TMS	JTAG5	A07
A08	_	+3.3v	
	+3.3 volt power		A09
A10	+3.3 volt power	+3.3v	A10
A11	Fundamental reset	PERST#	A11
A 4 0	Mechanical Key	IONE	1 440
A12	Ground	GND	A12
A13	Reference Clock, Differential	REFCLK+	A13
A14	pair	REFCLK-	A14
A15	Ground	GND	A15
A16	Receiver Lane 0, Differential pair	PERp(0)	A16
A 17	ixeceiver Lane 0, Dinerentiai pan	PERn(0)	A17
A18	Ground	GND	A18
A19	Manufacturer Test Mode	MFG	A19
A20	Ground	GND	A20
A21	Descional and 4 Differential nain	PERp(1)	A21
A22	Receiver Lane 1, Differential pair	PERn(1)	A22
A23	Ground	GND	A23
A24	Ground	GND	A24
A25	Receiver Lane 2, Differential pair	PERp(2)	A25
A26		PERn(2)	A26
A27	Ground	GND	A27
A28	Ground	GND	A28
A29		PERp(3)	A29
A30	Receiver Lane 3, Differential pair	pair PERn(3)	A30
A31	Ground	GND	A31
A32	Reserved	RSVD	A32
A33	Reserved	RSVD	A33
A34	Ground	GND	A34
A35		PERp(4)	A35
A36	Receiver Lane 4, Differential pair	PERn(4)	A36
A37	Ground	GND	A37
A38	Ground	GND	A38
A39		PERp(5)	A39
A40	Receiver Lane 5, Differential pair	PERn(5)	A39
A40 A41	Ground	GND	A40
A41 A42	Ground	GND	A41 A42
A42 A43		PERp(6)	A42
	Receiver Lane 6, Differential pair	PERP(6)	
A44	, , , , , , , , , , , , , , , , , , ,	PERN(6)	A44
A45	Ground	GND	A45
A46	Ground	GND	A46
A47	Receiver Lane 7, Differential pair	PERp(7)	A47
A48	•	PERII(1)	A48
A49	Ground	GND	A49

ALL GROUNDS ARE TIED TOGETHER IN PCBS

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